

Abstracts

Integrated Gigabit Logic -- State of the Art Assessment and Performance Projections

L.J. Micheel and G.G. Rabanus. "Integrated Gigabit Logic -- State of the Art Assessment and Performance Projections." 1978 MTT-S International Microwave Symposium Digest 78.1 (1978 [MWSYM]): 50-53.

After an overview of a wide range of technologies featuring sub-nanosecond ring oscillator stage delays, a simple method is described to convert such data into gate delays and flipflop clock rates in larger monolithic integrated circuits. Performance and chip complexity advancement for 1980 and 1985 are projected.

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